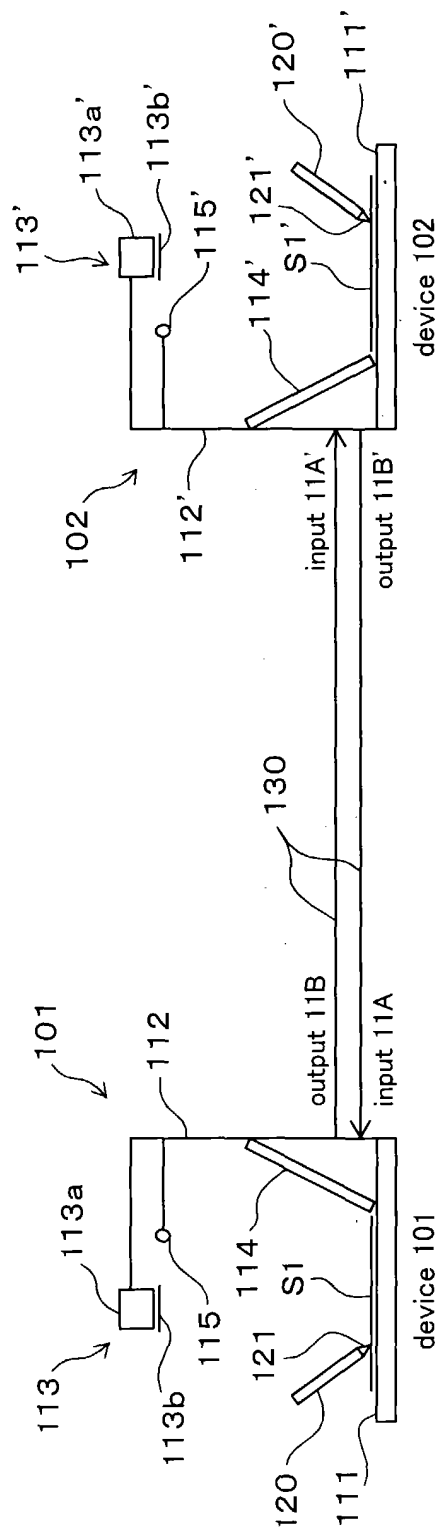


Fig. 1



main image is image A1 by device 101	input 11A	output 11B' of device 102
	display	image A1+a1+b1
main image is image B1 by device 102	output 11B	image A1+a1
	input 11A	output 11B' of device 102
	display	image B1+a1+b1
	output 11B	image B1+a1+b1

main image is image A1 by device 101	input 11A'	output 11B of device 101
	display	image A1+a1+b1
main image is image B1 by device 102	output 11B'	image A1+a1+b1
	input 11A'	output 11B of device 101
	display	image B1+a1+b1
	output 11B'	image B1+a1+b1

Fig. 2

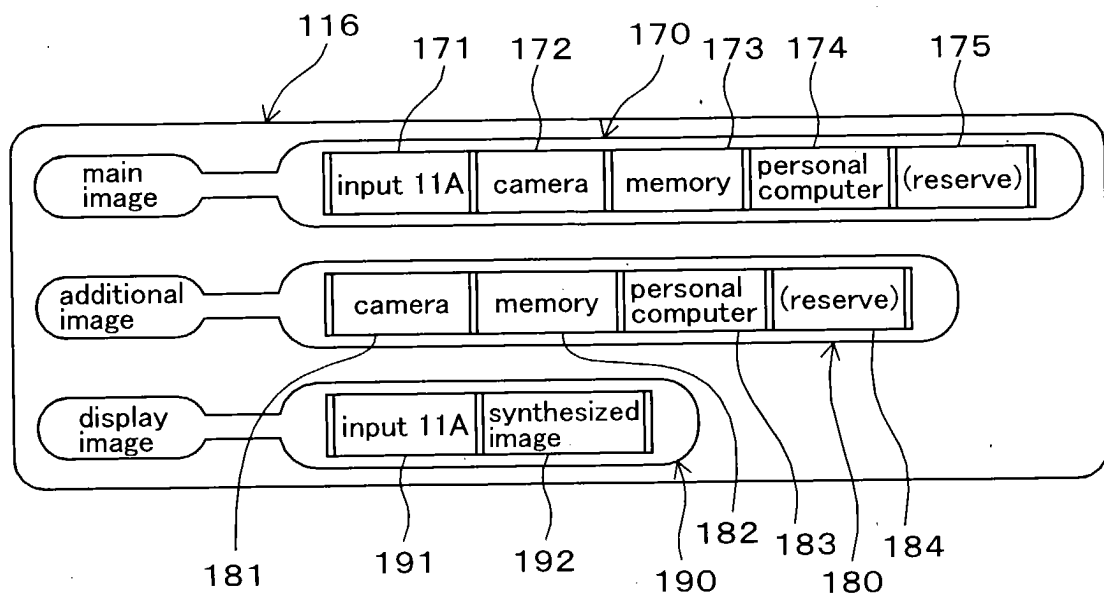


Fig. 3A

functional block diagram  
of device 101

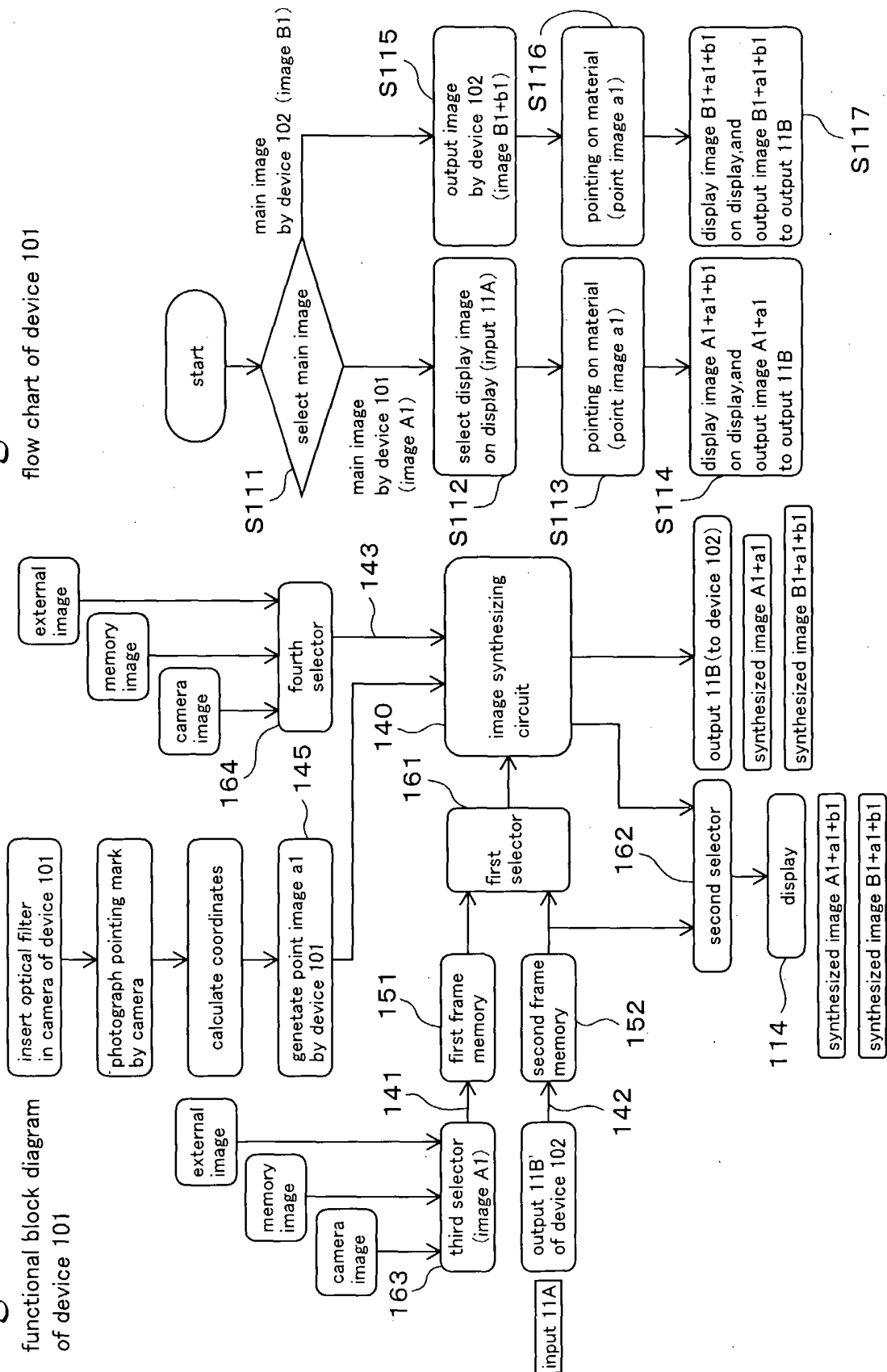


Fig. 3B

flow chart of device 101

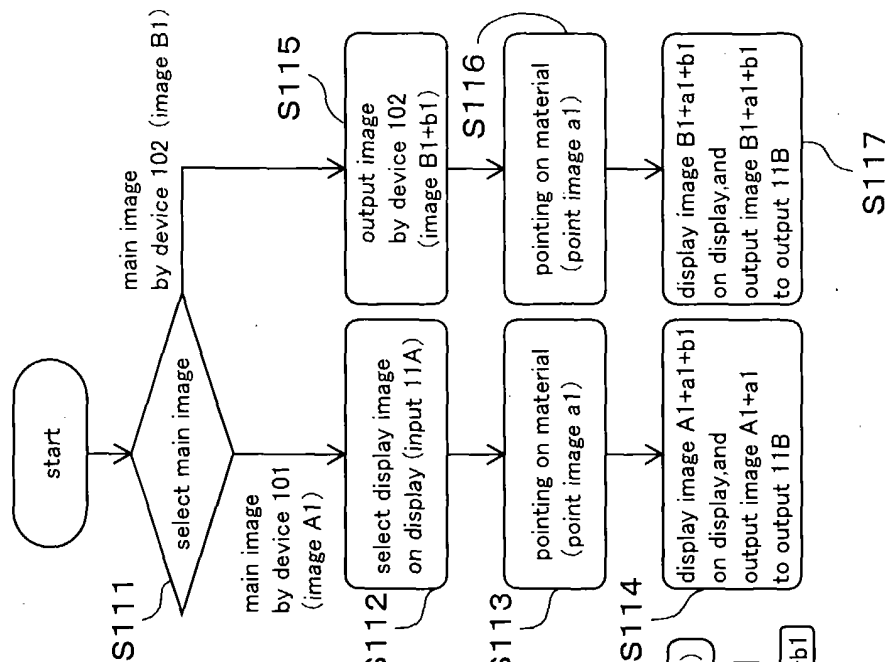


Fig. 4A

functional block diagram  
of device 102

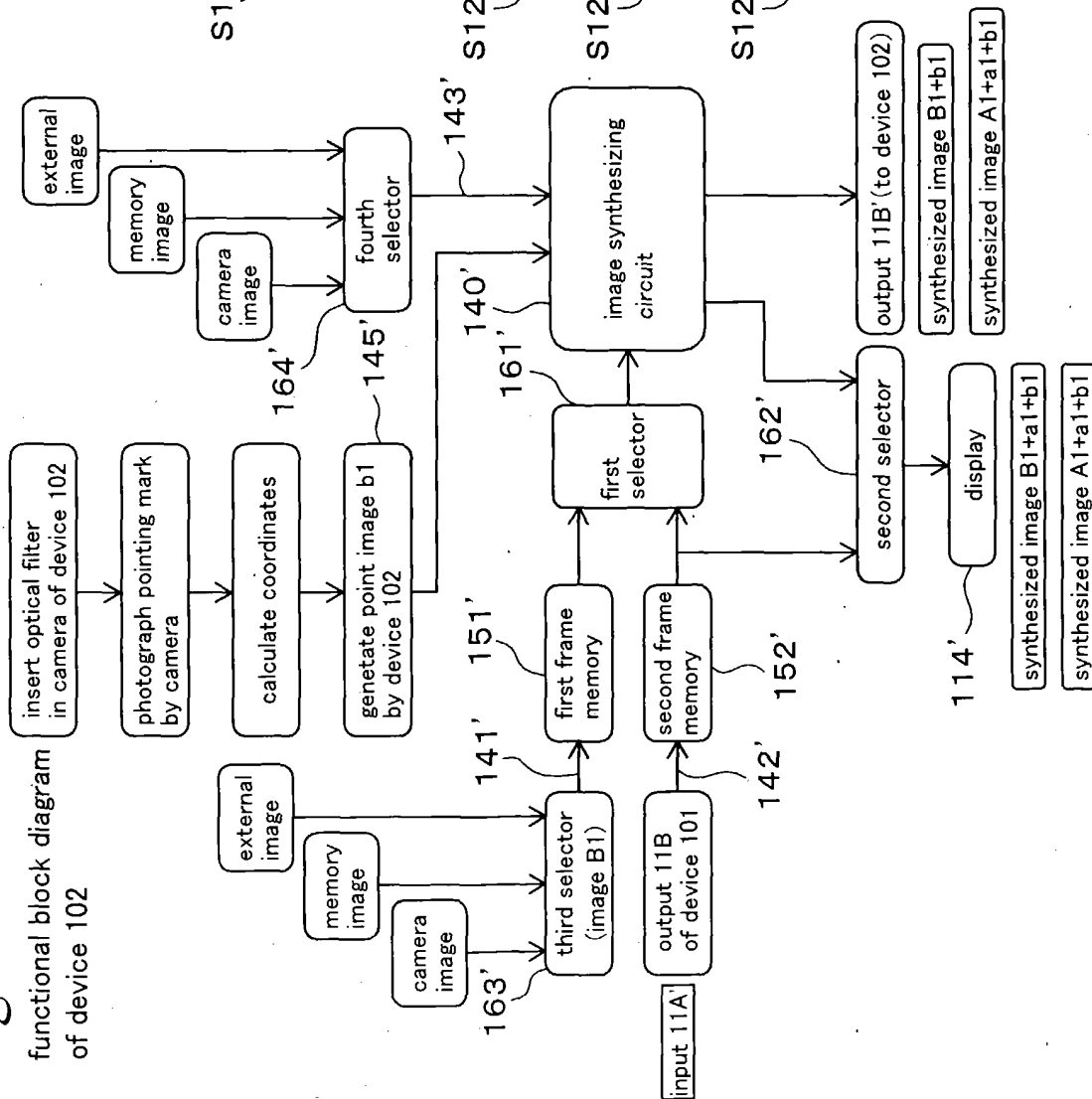


Fig. 4B

flow chart of device 102

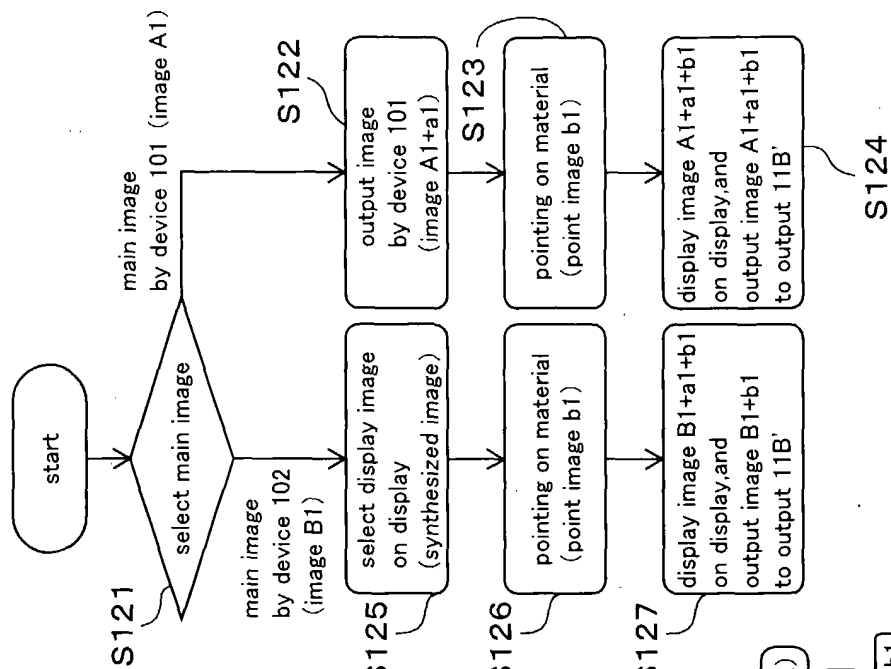


Fig. 5

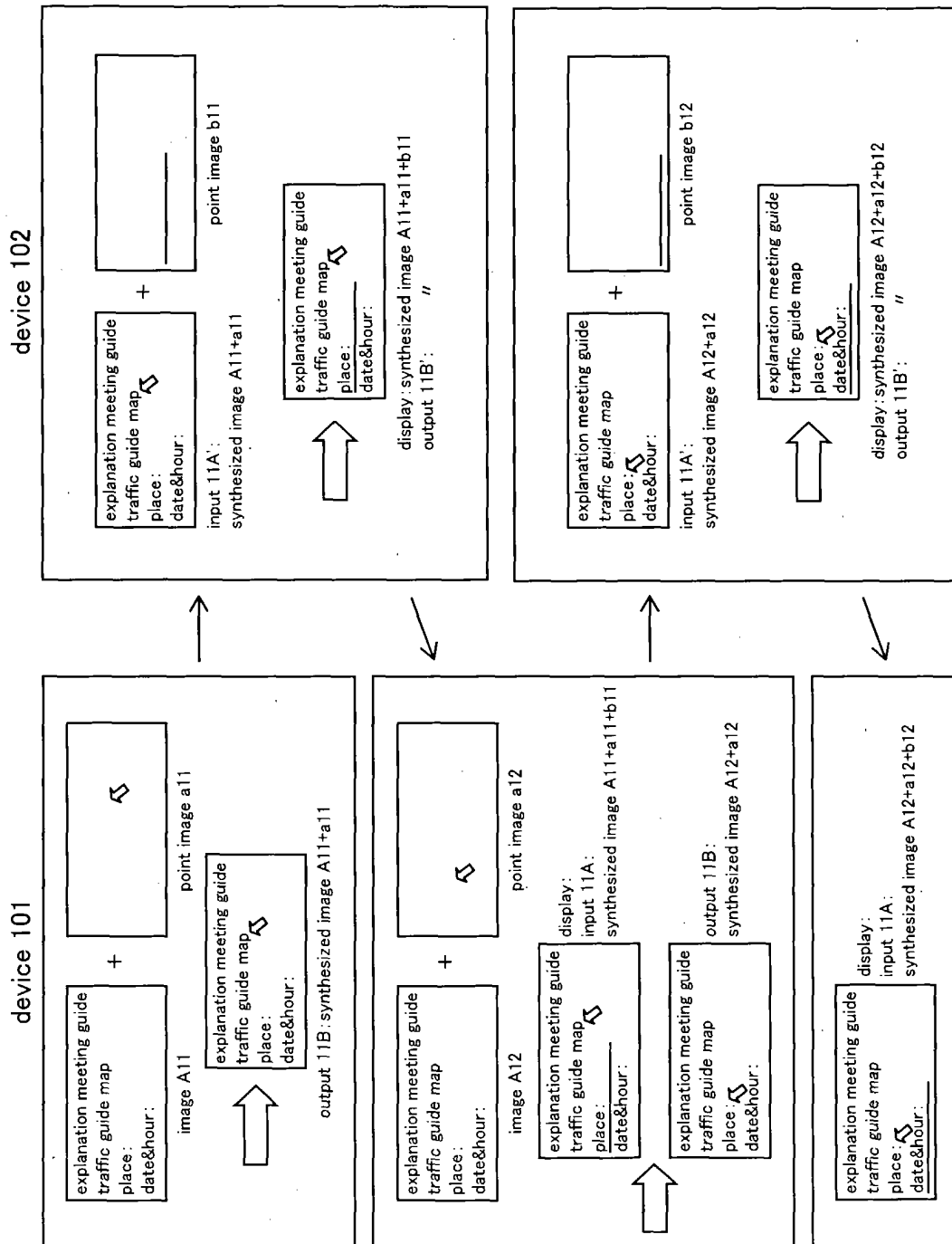


Fig. 6

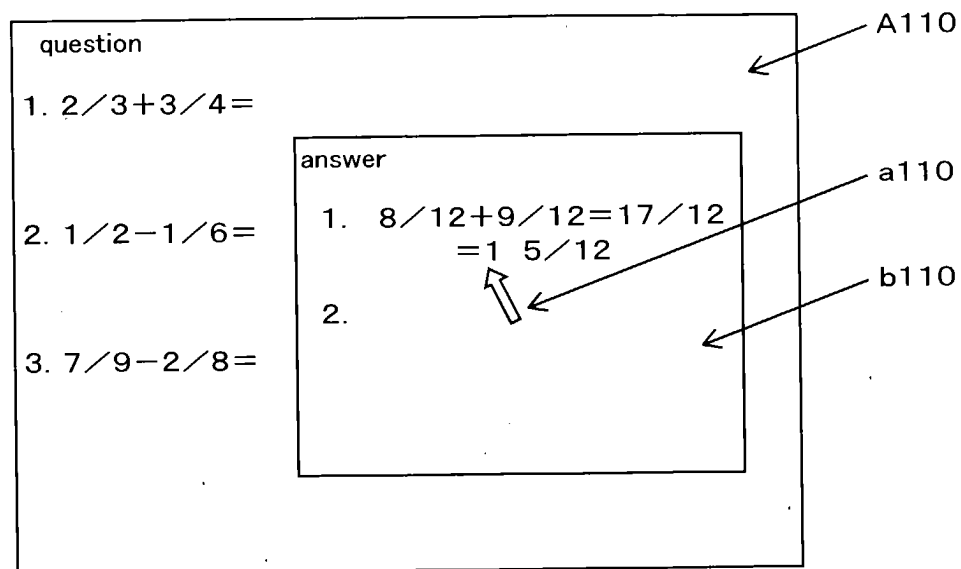


Fig. 7

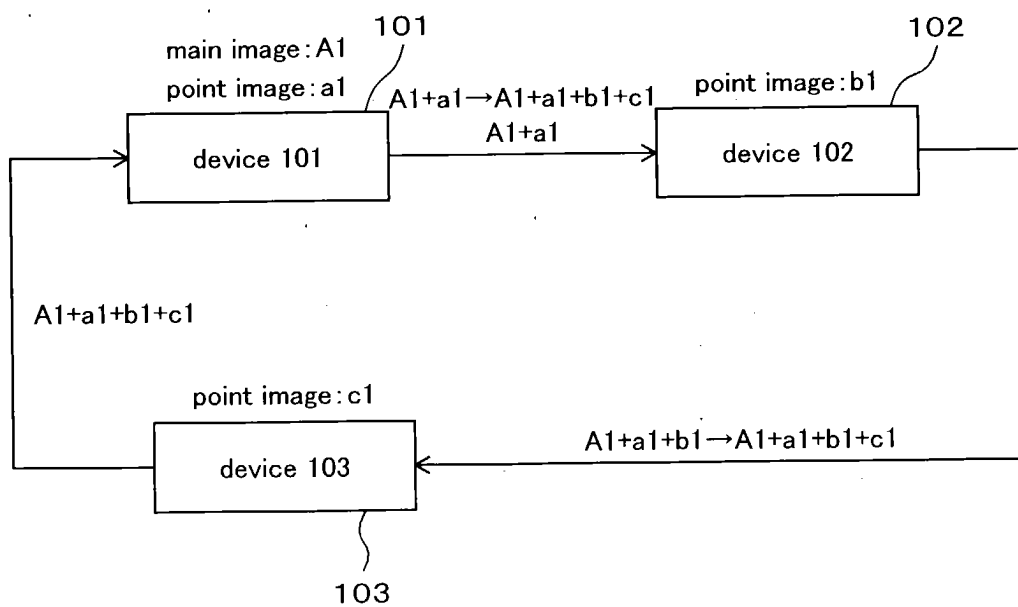


Fig. 8

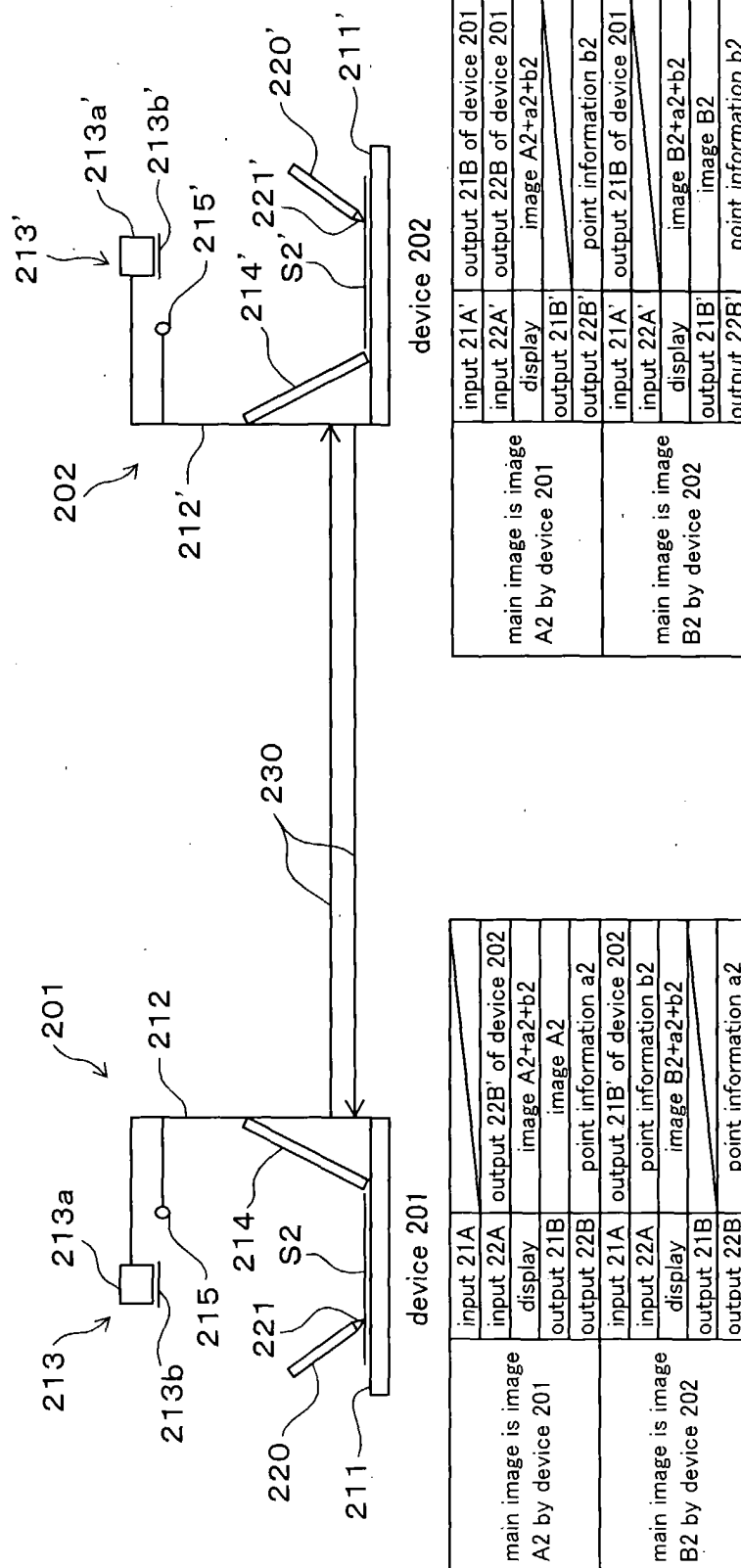


Fig. 9

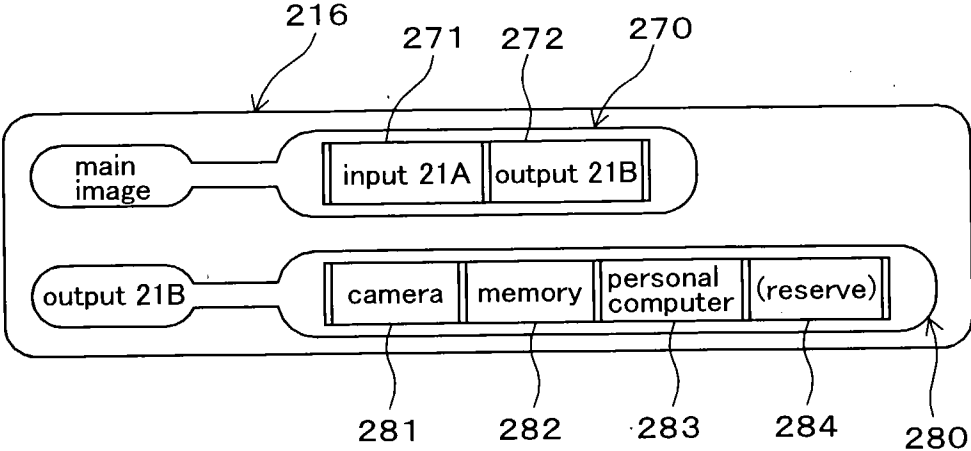




Fig. 10A

functional block diagram  
of device 201

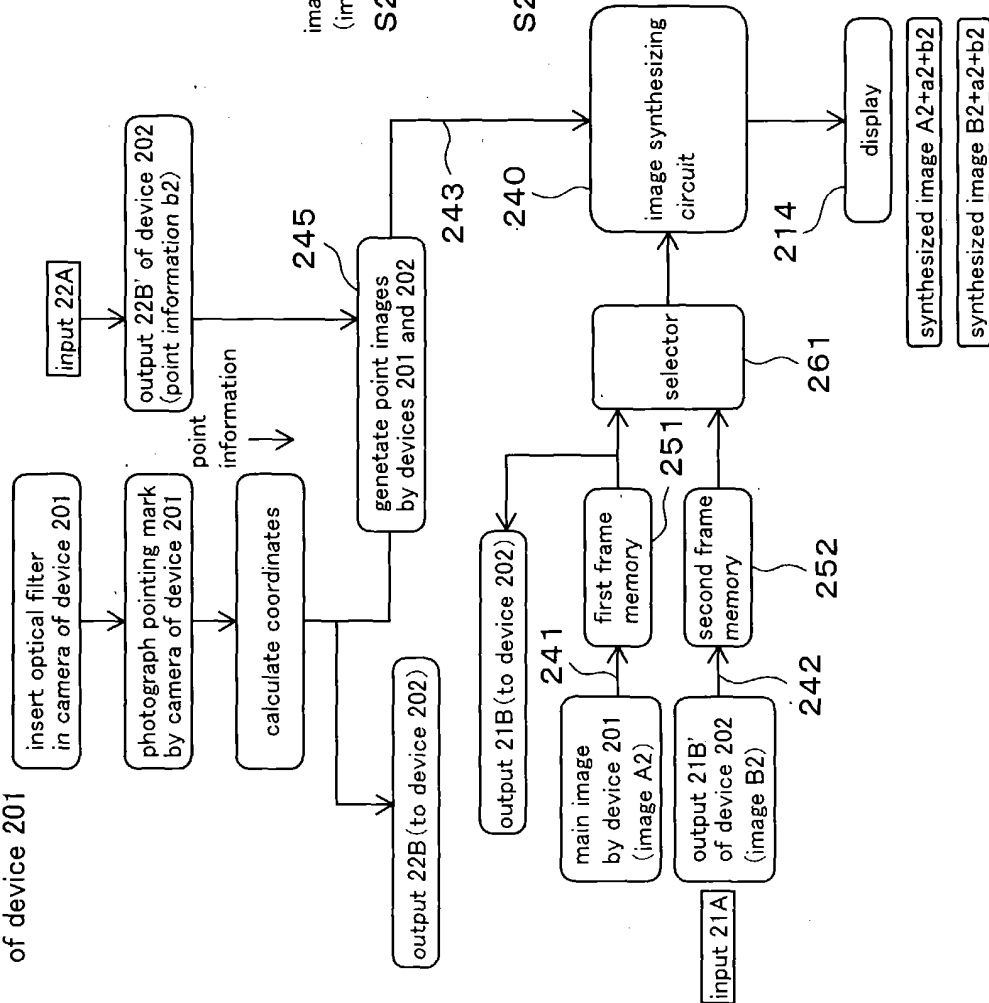


Fig. 10B

flow chart of device 201

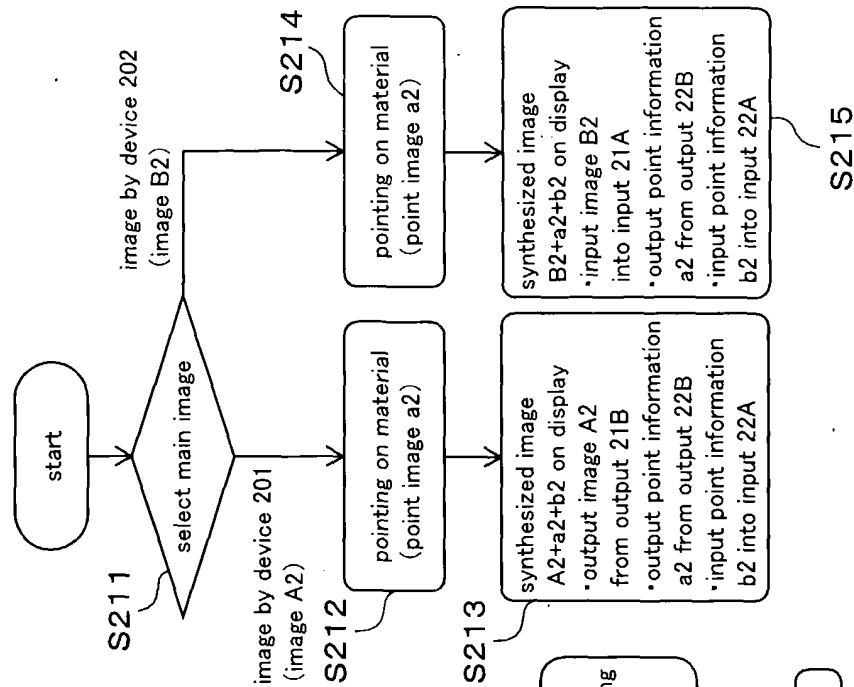


Fig. 11A

functional block diagram  
of device 202

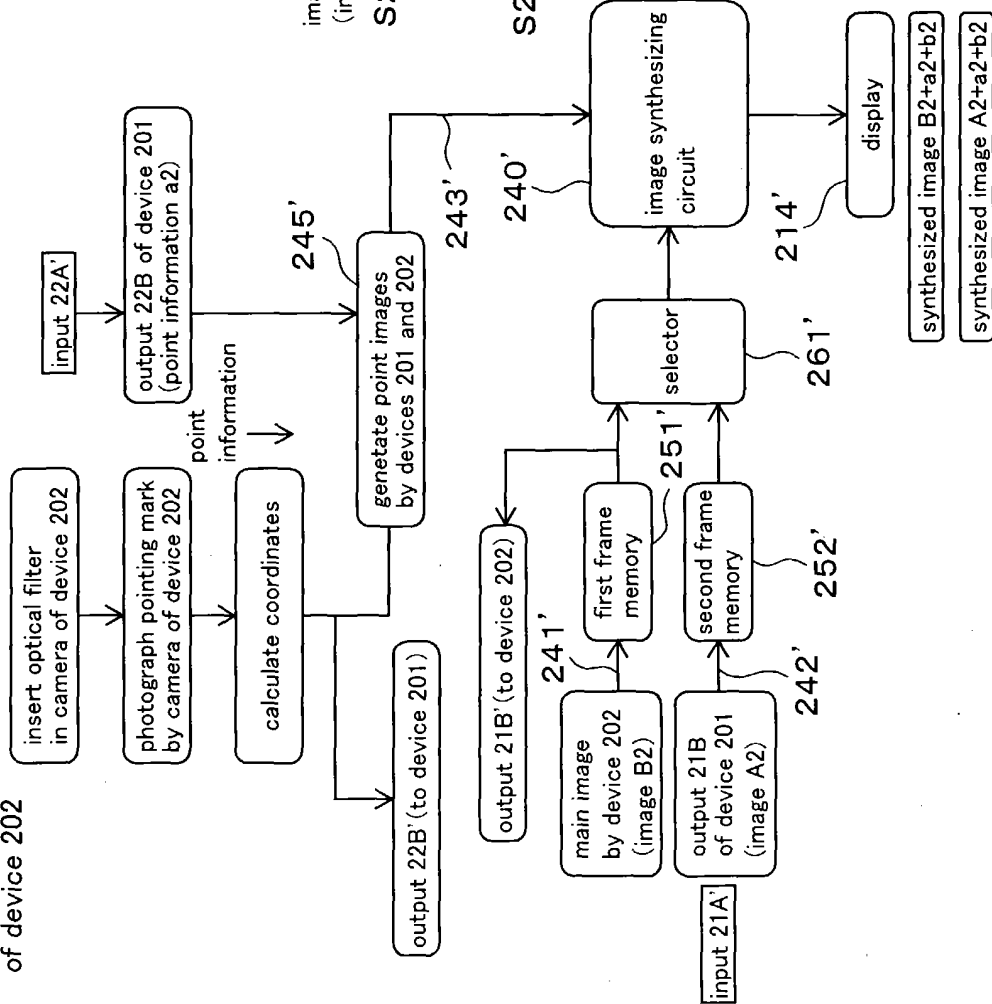


Fig. 11B

flow chart of device 202

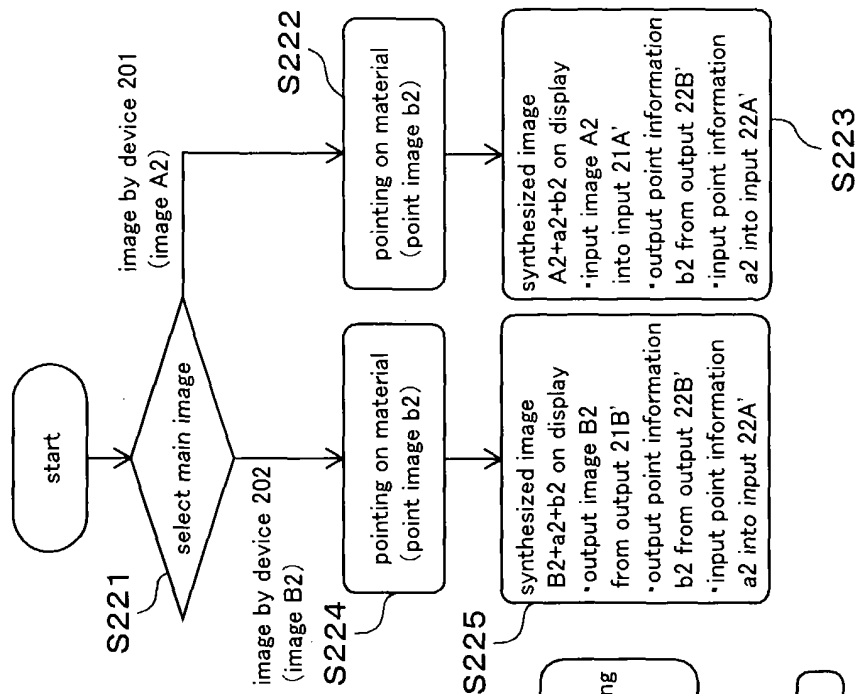
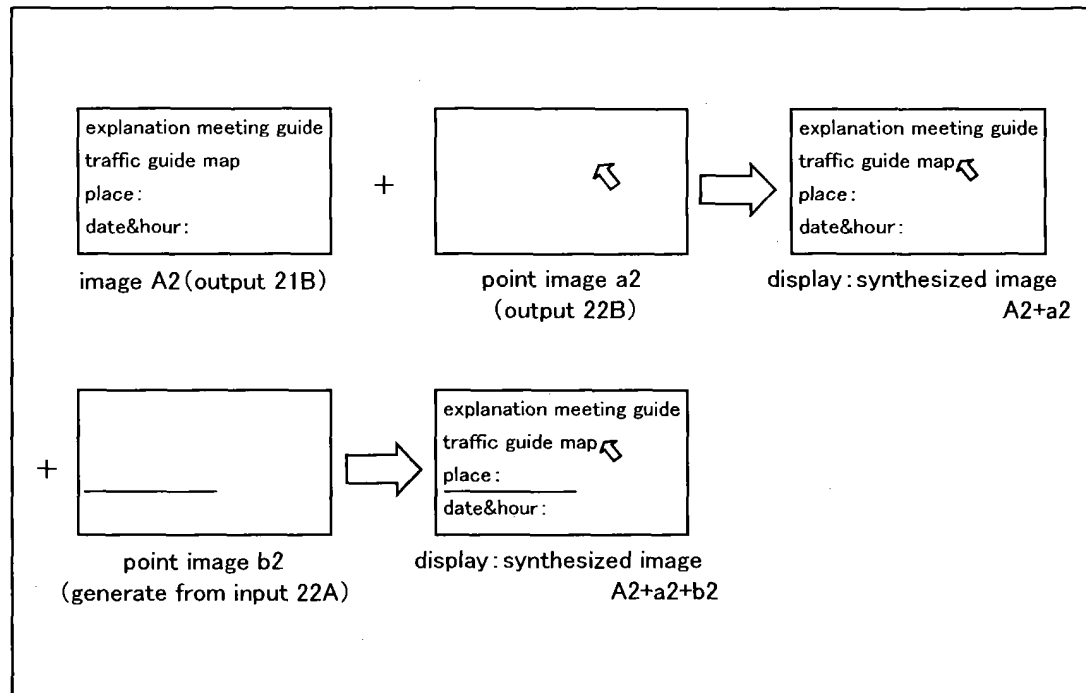


Fig. 12

device 201



device 202

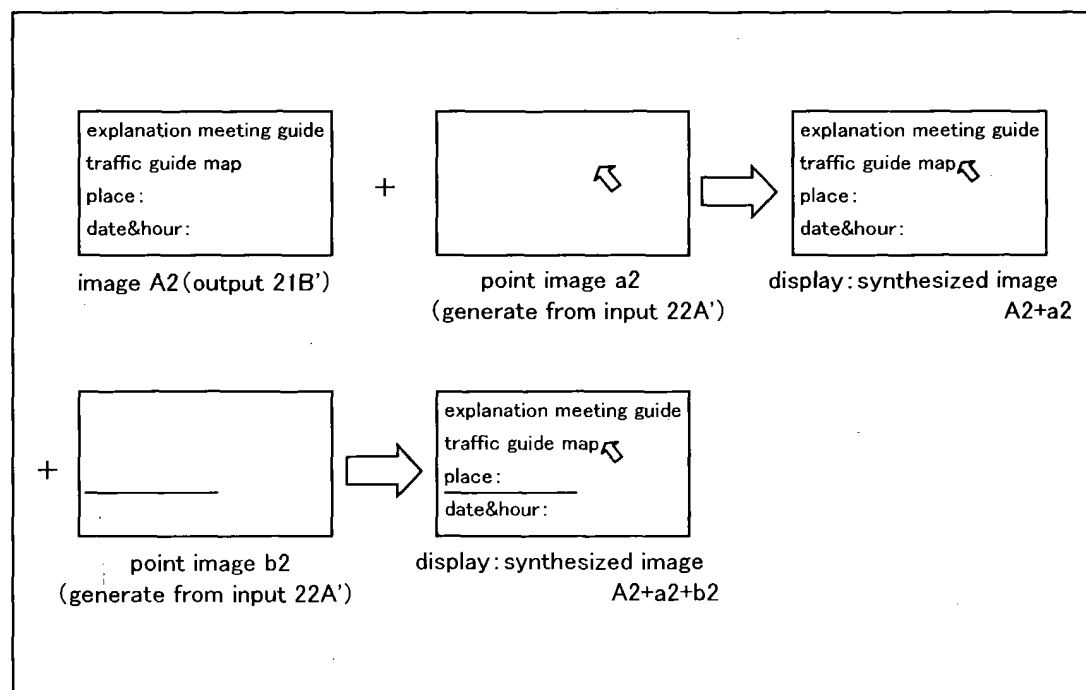


Fig. 13

